■ MN101C35D

Туре	MN101C35D				
ROM (x8-bit)	64 K				
RAM (×8-bit)	2 K				
Package	QFP100-P-1818B *Lead-free				
Minimum Instruction Execution Time	0.25 μs (at 2.7 V to 5.5 V, 8 MHz) 125 μs (at 2.2 V to 5.5 V, 32 kHz)* * The lower limit for operation guarantee for EPROM built-in type is 2.7 V.				
Interrupts	• RESET • Watchdog • External 0 • External 1 • External 2 • External 3 • External 4 • Timer 0 • Timer 1 • Timer 2 • Timer 3 • Timer 4 • Timer 5 • Time base • Serial 0 • Serial 1 • Serial 2 • Automatic transfer finish • A/D conversion finish • Key scan				
Timer Counter	Timer counter 0: 8-bit × 1 (square-wave/8-bit PWM output, event count, generation of remote control carrier) Clock source				
	Timer counter 1: 8-bit × 1 (square-wave output, event count, synchronous output event) Clock source				
	Timer counter 0, 1 can be cascade-connected.				
	Timer counter 2: 8-bit × 1 (square-wave/8-bit PWM output, event count, synchronous output event) Clock source				
	external clock input Interrupt source coincidence with compare register 3				
	Timer counter 2, 3 can be cascade-connected.				
	Timer counter 4: 16-bit × 1 (square-wave/16-bit PWM output, event count, synchronous output event, input capture) Clock source				
	Time base timer (one-minute count setting, independently operable 8-bit timer counter 5) Clock source				
	Watchdog timer Interrupt source 1/2097152 of system clock frequency				
Serial Interface	Serial 0 : synchronous type/simple UART (half-duplex) × 1 Clock source ··················· 1/2, 1/4, 1/16 of system clock frequency; 1/2 of timer counter 3 frequency				
	Serial 1: synchronous type × 1 Clock source				
	Serial 2: synchronous type/simple $I^2C \times 1$ Clock source				

1 Panasonic MAD00024DEM

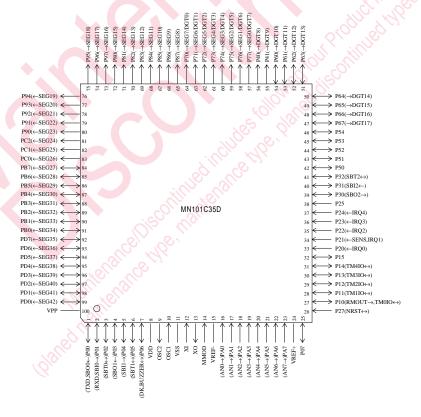
	I/O Pins I/O		36	• Common use: 28 • Specified pull-up resistor available • Input/output selectable (bit unit)		
	High Voltage		53	• Output: 29 • I/O: 24 • P-ch open drain (breakdown voltage -30V): FL drive: 53 • Specified pull-down resistor mask option: 35		
	A/D Inputs 8 -bit \times 8-ch. (with S/H)					
	FL	(35 to 43) segments × (18 to 10) digits				
	Special Ports B		Buzz	uzzer output, remote control carrier signal output		
Г	Flootrical Characteristics					

Electrical Characteristics

Supply current

Parameter	Symbol	Condition		Limit		
raiaillelei	Symbol			typ	max	Unit
Operating cumply ourrent	IDD1	fosc = 8 MHz, VDD = 5 V			25	mA
Operating supply current	IDD2	fx = 32 kHz, VDD = 3 V			120	μА
Supply current at HALT	IDD3	fx = 32 kHz, VDD = 3 V		0.	10	μА
Supply current at STOP	IDD4	VDD = 3 V	10.5	os ill	10	μА

Pin Assignment



QFP100-P-1818B *Lead-free

Support Tool

In-circuit Emulator	PX-ICE101C / D + PX-PRB101C35-QF	PX-ICE101C / D + PX-PRB101C35-QFP100-P-1818B			
EPROM Built-in Type	Туре	MN101CP35D			
	ROM (× 8-bit)	64 K			
	RAM (× 8-bit)	2 K			
	Minimum instruction execution time	0.25 μs (at 2.7 V to 5.5 V, 8 MHz)			
		125 μs (at 2.7 V to 5.5 V, 32 kHz)			
	Package	QFP100-P-1818B *Lead-free			



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