# **Communication infrastructure**

#### Set product trend

- High-speed communication (- 40 Gbps)
- Increase in the number of ports and data throughput
- Slimming (1U)

Power source trend

Increase in LSI load current

Larger LSI current & Increased quantity of LSI <Output>

• FPGA/ASIC (1 V/ - 70A)

D size

• Other LSIs (1 V/ - 20A)

Capacitor required specifications

Low ESR

Large capacitance

Low profile

Features of recommended products

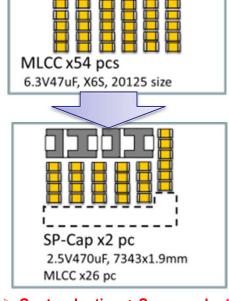
### SP-Cap

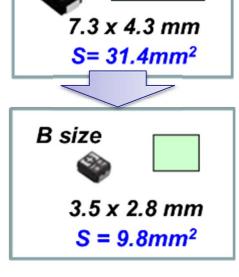
- Ultra-low ESR (3 mΩ )
- Low profile (1-2 mm)

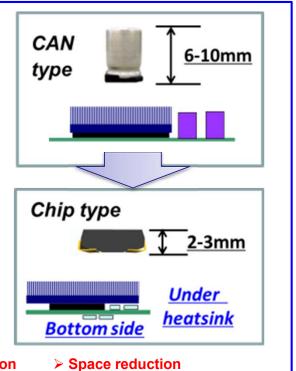
#### POSCAP

- Low ESR (5 m $\Omega$  )
- Downsizing (3.5x2.8 mm)

## Proposals







> Cost reduction + Space reduction

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